

## TC1200 | TC3 PLC

TwinCAT PLC realises one or more PLCs with the international standard IEC 61131-3 3rd edition on one CPU. All programming languages described in the standard can be used for programming. The blocks of the type PROGRAM can be linked with real-time tasks. Various convenient debugging options facilitate fault-finding and commissioning. Program modifications can be carried out at any times and in any size online, i.e. when the PLC is running. All variables are available symbolically by ADS and can be read and written in appropriate clients.

- process image size, flag range, program size, POU size and number of variables are limited only by size of RAM
- cycle times from 50 µs
- link time: typically 1 μs (Intel® Core™2 Duo) for 1,000 commands
- IEC 61131-3: IL, FBD, LD, SFC, ST, CFC
- online changes in programs and variables
- remote debugging via TCP/IP
- online connection with PLC runtime system worldwide via TCP/IP or fieldbus
- online monitoring of variables in variable lists, watch windows, editors
- online status and powerflow (accumulator contents) of programs and instances
- triggering, forcing and setting variables
- powerful debugging with single cycle, break points, step in, step over, display of the current call stack, watchlist shows selection of variable, trace functions
- online management of all variable names and structures across the whole system
- remanent and persistent data, UPS supported storage on hard disk, storage in NOVRAM as option
- variable reading and writing access via ADS, OPC
- certified in accordance with PLCopen base level (IL/ST)
- structured programming with modular program management
- source code is stored in the target system
- convenient library management
- powerful compiler with incremental compilation
- all common data types, structures, arrays, including multi-dimensional arrays
- convenient creation of programs with: autoformat, autodeclare, cross-reference, search/replace, project comparison
- simple linking to source code administration tools by embedding in Microsoft Visual Studio®

Technical data	TC1200
Target system	Windows XP, Windows 7/8/10, Windows CE

Ordering information	
TC1200-0020	TC3 PLC, platform 20 (Economy)
TC1200-0030	TC3 PLC, platform 30 (Economy Plus)
TC1200-0040	TC3 PLC, platform 40 (Performance)
TC1200-0050	TC3 PLC, platform 50 (Performance Plus)
TC1200-0060	TC3 PLC, platform 60 (Mid Performance)
TC1200-0070	TC3 PLC, platform 70 (High Performance)
TC1200-0080	TC3 PLC, platform 80 (Very High Performance)
TC1200-0081	TC3 PLC, platform 81 (Many-core 58 cores)
TC1200-0082	TC3 PLC, platform 82 (Many-core 916 cores)
TC1200-0083	TC3 PLC, platform 83 (Many-core 1732 cores)
TC1200-0084	TC3 PLC, platform 84 (Many-core 3364 cores)
TC1200-0090	TC3 PLC, platform 90 (Other)
TC1200-0091	TC3 PLC, platform 91 (Other 58 cores)
TC1200-0092	TC3 PLC, platform 92 (Other 916 cores)

TC1200-0093	TC3 PLC, platform 93 (Other 1732 cores)
TC1200-0094	TC3 PLC, platform 94 (Other 3364 cores)