SIEMENS

| Product data sheet | 6ES7312-5BF04-0AB0 |
|--|--|
| | SIMATIC S7-300, CPU 312C COMPACT CPU WITH MPI, 10 DI/6 DO, 2 FAST COUNTERS (10 KHZ), INTEGRATED 24V DC POWER SUPPLY, 64 KBYTE WORKING MEMORY, FRONT CONNECTOR (1 X 40PIN) AND MICRO MEMORY CARD REQUIRED |
| General information | |
| Hardware product version | 01 |
| Firmware version | V3.3 |
| Engineering with | |
| Programming package | STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203 |
| Supply voltage | |
| 24 V DC | Yes |
| permissible range, lower limit (DC) | 19.2 V |
| permissible range, upper limit (DC) | 28.8 V |
| External protection for supply cables (recommendation) | Miniature circuit breaker, type C; min 2 A; miniature circuit breaker type B, min. 4 A |
| Mains buffering | |
| Mains/voltage failure stored energy time | 5 ms |
| Repeat rate, min. | 1 s |
| Digital outputs | |
| Load voltage L+ | |
| Rated value (DC) | 24 V |
| Reverse polarity protection | No |
| Input current | |
| Current consumption (rated value) | 570 mA |
| Current consumption (in no-load operation), typ. | 90 mA |
| Inrush current, typ. | 5 A |

| | 0.7 A ² ·s |
|---|---|
| Digital outputs | |
| from load voltage L+, max. | 25 mA |
| Power losses | |
| Power loss, typ. | 8 W |
| Memory | |
| Work memory | |
| integrated | 64 Kibyte |
| expandable | No |
| Size of retentive memory for retentive data blocks | 64 Kibyte |
| Load memory | |
| pluggable (MMC) | Yes |
| pluggable (MMC), max. | 8 Mbyte |
| Data management on MMC (after last programming), min. | 10 a |
| Backup | |
| present | Yes ; Guaranteed by MMC (maintenance-free) |
| without battery | Yes ; Program and data |
| CPU processing times | |
| for bit operations, min. | 0.1 μs |
| for word operations, min. | 0.24 µs |
| for fixed point arithmetic, min. | 0.32 µs |
| for floating point arithmetic, min. | 1.1 µs |
| CPU-blocks | |
| Number of blocks (total) | 1024 ; (DBs, FCs, FBs); the maximum number of loadable blocks can be reduced by the MMC used. |
| DB | |
| Number, max. | 1024 ; Number range: 1 to 16000 |
| Size, max. | 64 Kibyte |
| FB | |
| Number, max. | 1024 ; Number range: 0 to 7999 |
| Size, max. | 64 Kibyte |
| FC | |
| Number, max. | 1024 ; Number range: 0 to 7999 |

| Size, max. | 64 Kibyte |
|---|-----------------------|
| ОВ | |
| Description | see instruction list |
| Size, max. | 64 Kibyte |
| Number of free cycle OBs | 1 ; OB 1 |
| Number of time alarm OBs | 1 ; OB 10 |
| Number of delay alarm OBs | 2 ; OB 20, 21 |
| Number of time interrupt OBs | 4 ; OB 32, 33, 34, 35 |
| Number of process alarm OBs | 1 ; OB 40 |
| Number of startup OBs | 1 ; OB 100 |
| Number of asynchronous error OBs | 4 ; OB 80, 82, 85, 87 |
| Number of synchronous error OBs | 2 ; OB 121, 122 |
| Nesting depth | |
| per priority class | 16 |
| additional within an error OB | 4 |
| Counters, timers and their retentivity | |
| S7 counter | |
| Number | 256 |
| Retentivity | |
| adjustable | Yes |
| lower limit | 0 |
| upper limit | 255 |
| preset | Z 0 to Z 7 |
| Counting range | |
| Counting range | |
| lower limit | 0 |
| | 999 |
| lower limit | |
| lower limit upper limit | |
| lower limit upper limit IEC counter | 999 |
| lower limit upper limit IEC counter present | 999 Yes |
| lower limit upper limit IEC counter present Type | 999 Yes SFB |
| lower limit upper limit IEC counter present Type Number | 999 Yes SFB |

| adjustable | Yes |
|----------------------------------|--|
| lower limit | 0 |
| | |
| upper limit | 255 |
| preset | No retentivity |
| Time range | |
| lower limit | 10 ms |
| upper limit | 9990 s |
| IEC timer | |
| present | Yes |
| Туре | SFB |
| Number | Unlimited (limited only by RAM capacity) |
| Data areas and their retentivity | |
| retentive data area, total | All, max. 64 KB |
| Flag | |
| Number, max. | 256 byte |
| Retentivity available | Yes ; MB 0 to MB 255 |
| Retentivity preset | MB 0 to MB 15 |
| Number of clock memories | 8 ; 1 memory byte |
| Data blocks | |
| Number, max. | 1024 ; Number range: 1 to 16000 |
| Size, max. | 64 Kibyte |
| Retentivity adjustable | Yes ; via non-retain property on DB |
| Retentivity preset | Yes |
| Local data | |
| per priority class, max. | 32 Kibyte ; Max. 2048 bytes per block |
| Address area | |
| I/O address area | |
| Inputs | 1024 byte |
| Outputs | 1024 byte |
| of which, distributed | |
| Inputs | none |
| Outputs | none |
| Process image | |
| | |

| | 10041 |
|--|----------------|
| Inputs | 1024 byte |
| Outputs | 1024 byte |
| Inputs, adjustable | 1024 byte |
| Outputs, adjustable | 1024 byte |
| Inputs, default | 128 byte |
| Outputs, default | 128 byte |
| Default addresses of the integrated channels | |
| Digital inputs | 124.0 to 125.1 |
| Digital outputs | 124.0 to 124.5 |
| Digital channels | |
| Inputs | 266 |
| Outputs | 262 |
| Inputs, of which central | 266 |
| Outputs, of which central | 262 |
| Analog channels | |
| Inputs | 64 |
| Outputs | 64 |
| Inputs, of which central | 64 |
| Outputs, of which central | 64 |
| Hardware configuration | |
| Racks, max. | 1 |
| Modules per rack, max. | 8 |
| Expansion devices, max. | 0 |
| Number of DP masters | |
| integrated | none |
| via CP | 4 |
| Number of operable FMs and CPs (recommended) | |
| FM | 8 |
| CP, point-to-point | 8 |
| CP, LAN | 4 |
| Time of day | |
| Clock | |
| Software clock | Yes |
| | |

| battery-backed and synchronizable | No ; Buffered No Can be synchronized Yes |
|---|---|
| Deviation per day, max. | 10 s; Typ.: 2 s |
| Behavior of the clock following POWER-ON | The clock continues at the time of day it had when power was switched off |
| Operating hours counter | |
| Number | 1 |
| Number/Number range | 0 |
| Range of values | 0 to 2^31 hours (when using SFC 101) |
| Granularity | 1 hour |
| retentive | Yes ; Must be restarted at each restart |
| Clock synchronization | |
| supported | Yes |
| to MPI, master | Yes |
| to MPI, slave | Yes |
| in AS, master | Yes |
| in AS, slave | No |
| Digital inputs | |
| Number/binary inputs | 10 |
| of which, inputs usable for technological functions | 8 |
| integrated channels (DI) | 10 |
| Input characteristic curve acc. to IEC 1131, Type 1 | Yes |
| Number of simultaneously controllable inputs | |
| horizontal installation | |
| up to 40 °C, max. | 10 |
| up to 60 °C, max. | 5 |
| vertical installation | |
| up to 40 °C, max. | 5 |
| Technological functions | |
| shielded, max. | 100 m ; at maximum count frequency |
| Unshielded, max. | not allowed |
| Standard DI | |
| shielded, max. | 1000 m |
| Unshielded, max. | 600 m |
| Input voltage | |
| | |

| Type of input voltage | |
|--|---|
| Rated value, DC | 24 V |
| for signal "0" | -3 to +5 V |
| for signal "1" | 15 to 30 V |
| Input current | |
| for signal "1", typ. | 8 mA |
| Input delay (for rated value of input voltage) | |
| for standard inputs | |
| Parameterizable | Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances your newly set filter time may not be effective until the next filter cycle.) |
| Rated value | 3 ms |
| for counter/technological functions | |
| at "0" to "1", max. | 48 μs; Minimum pulse width/minimum pause between pulses at maximum counting frequency |
| Cable length | |
| Cable length, shielded, max. | 1000 m; 100 m for technological functions |
| Cable length unshielded, max. | 600 m ; For technological functions: No |
| Digital outputs | |
| Number/binary outputs | 6 |
| of which high-speed outputs | 2 ; Notice: You cannot connect the fast outputs of your CPU in parallel |
| integrated channels (DO) | 6 |
| Functionality/short-circuit strength | Yes ; Clocked electronically |
| Response threshold, typ. | 1 A |
| Limitation of inductive shutdown voltage to | L+ (-48 V) |
| Lamp load, max. | 5 W |
| Controlling a digital input | Yes |
| Output voltage | |
| for signal "1", min. | L+ (-0.8 V) |
| Output current | |
| for signal "1" rated value | 500 mA |
| for signal "1" permissible range, min. | 5 mA |

| | _ |
|--|---------|
| for signal "1" permissible range, max. | 0.6 A |
| for signal "1" minimum load current | 5 mA |
| for signal "0" residual current, max. | 0.5 mA |
| Parallel switching of 2 outputs | |
| for increased power | No |
| for redundant control of a load | Yes |
| Switching frequency | |
| with resistive load, max. | 100 Hz |
| with inductive load, max. | 0.5 Hz |
| on lamp load, max. | 100 Hz |
| of the pulse outputs, with resistive load, max. | 2.5 kHz |
| Aggregate current of outputs (per group) | |
| horizontal installation | |
| up to 40 °C, max. | 2 A |
| up to 60 °C, max. | 1.5 A |
| vertical installation | |
| up to 40 °C, max. | 1.5 A |
| Load resistance range | |
| lower limit | 48 Ω |
| upper limit | 4 kΩ |
| Cable length | |
| Cable length, shielded, max. | 1000 m |
| Cable length unshielded, max. | 600 m |
| Analog inputs | |
| Integrated channels (AI) | 0 |
| Analog outputs | |
| Integrated channels (AO) | 0 |
| Encoder | |
| Connectable encoders | |
| 2-wire BEROS | Yes |
| permissible quiescent current (2-wire BEROS), max. | 1.5 mA |
| Interfaces | |
| Number of USB interfaces | 0 |
| | |

| Number of parallel interfaces | 0 |
|---|--|
| Number of 20 mA interfaces (TTY) | 0 |
| Number of RS 232 interfaces | 0 |
| Number of RS 422 interfaces | 0 |
| Number of other hardware interfaces | 0 |
| 1st interface | |
| Type of interface | Integrated RS 485 interface |
| Physics | RS 485 |
| Isolated | No |
| Power supply to interface (15 to 30 V DC), max. | 200 mA |
| Functionality | |
| MPI | Yes |
| DP master | No |
| DP slave | No |
| Point-to-point connection | No |
| MPI | |
| Services | |
| PG/OP communication | Yes |
| Routing | No |
| Global data communication | Yes |
| S7 basic communication | Yes |
| S7 communication | Yes; Only server, configured on one side |
| S7 communication, as client | No ; (but via CP and loadable FBs) |
| S7 communication, as server | Yes |
| | |
| Transmission rate, max. | 187.5 kbit/s |
| Transmission rate, max. Communication functions | 187.5 kbit/s |
| · | 187.5 kbit/s Yes |
| Communication functions | |
| Communication functions PG/OP communication | Yes |
| Communication functions PG/OP communication Data record routing | Yes |
| Communication functions PG/OP communication Data record routing Global data communication | Yes No |
| Communication functions PG/OP communication Data record routing Global data communication supported | Yes No Yes |
| Communication functions PG/OP communication Data record routing Global data communication supported Number of GD loops, max. | Yes No Yes 8 |

| Number of GD packets, receiver, max. | 8 |
|--|--|
| Size of GD packets, max. | 22 byte |
| Size of GD packet (of which consistent), max. | 22 byte |
| S7 basic communication | |
| supported | Yes |
| User data per job, max. | 76 byte |
| User data per job (of which consistent), max. | 76 byte; 76 bytes (with X_SEND or X_RCV); 64 bytes (with X_PUT or X_GET as server) |
| S7 communication | |
| supported | Yes |
| as server | Yes |
| as client | Yes ; Via CP and loadable FB |
| User data per job, max. | 180 byte ; (with PUT/GET) |
| User data per job (of which consistent), max. | 240 byte ; as server |
| S5-compatible communication | |
| supported | Yes ; via CP and loadable FC |
| Number of connections | |
| overall | 6 |
| usable for PG communication | 5 |
| reserved for PG communication | 1 |
| Adjustable for PG communication, min. | 1 |
| Adjustable for PG communication, max. | 5 |
| usable for OP communication | 5 |
| reserved for OP communication | 1 |
| adjustable for OP communication, min. | 1 |
| adjustable for OP communication, max. | 5 |
| usable for S7 basic communication | 2 |
| Reserved for S7 basic communication | 0 |
| adjustable for S7 basic communication, min. | 0 |
| adjustable for S7 basic communication, max. | 2 |
| S7 message functions | |
| Number of login stations for message functions, max. | 6 ; Depending on the configured connections for PG/OP and S7 basic communication |
| Process diagnostic messages | Yes |
| | |

| simultaneously active Alarm-S blocks, max. | 300 |
|--|---|
| Test commissioning functions | 300 |
| Status/control | |
| Status/control variable | Yes |
| | |
| Variables | Inputs, outputs, memory bits, DB, times, counters |
| Number of variables, max. | 30 |
| of which status variables, max. | 30 |
| of which control variables, max. | 14 |
| Forcing | |
| Forcing | Yes |
| Force, variables | Inputs, outputs |
| Number of variables, max. | 10 |
| Status block | Yes ; Up to 2 simultaneously |
| Single step | Yes |
| Number of breakpoints | 4 |
| Diagnostic buffer | |
| present | Yes |
| Number of entries, max. | 500 |
| adjustable | No |
| Of which powerfail-proof | 100 ; Only the last 100 entries are retained |
| Number of entries readable in RUN, max. | 499 |
| adjustable | Yes ; From 10 to 499 |
| preset | 10 |
| Service data | |
| Can be read out | Yes |
| Interrupts/diagnostics/status information | |
| Diagnostics indication LED | |
| Status indicator digital output (green) | Yes |
| Status indicator digital input (green) | Yes |
| Integrated Functions | |
| Number of counters | 2 ; See "Technological Functions" manual |
| Counter frequency (counter) max. | 10 kHz |
| Frequency measurement | Yes |
| | |

| Number of frequency meters | 2 ; up to 10 kHz (see "Technological Functions" manual) |
|--|---|
| controlled positioning | No |
| Integrated function blocks (closed-loop control) | No |
| PID controller | No |
| | |
| Number of pulse outputs | 2 ; Pulse width modulation up to 2.5 kHz (see "Technological Functions" Manual) |
| Limit frequency (pulse) | 2.5 kHz |
| Galvanic isolation | |
| Galvanic isolation digital inputs | |
| Galvanic isolation digital inputs | Yes |
| between the channels | No |
| between the channels and the backplane bus | Yes |
| Galvanic isolation digital outputs | |
| Galvanic isolation digital outputs | Yes |
| between the channels | No |
| between the channels and the backplane bus | Yes |
| Permissible potential difference | |
| between different circuits | 75 VDC / 60 VAC |
| Isolation | |
| Isolation checked with | 600 V DC |
| Ambient conditions | |
| Operating temperature | |
| Min. | 0 °C |
| max. | 60 °C |
| Configuration | |
| Configuration software | |
| STEP 7 Lite | No |
| programming | |
| Programming language | |
| LAD | Yes |
| FBD | Yes |
| STL | Yes |
| SCL | Yes |

| GRAPH | Yes |
|--|--|
| HiGraph® | Yes |
| Command set | see instruction list |
| Nesting levels | 8 |
| Software libraries | 0 |
| Software libraries | |
| System functions (SFC) | see instruction list |
| System function blocks (SFB) | see instruction list |
| Know-how protection | |
| User program protection/password protection | Yes |
| Block encryption | Yes ; With S7 block Privacy |
| Dimensions | |
| Width | 80 mm |
| Height | 125 mm |
| Depth | 130 mm |
| Weight | |
| Weight, approx. | 410 g |
| internTechDB | |
| Characteristics will be deleted after 2012-12-31 | |
| Product version | |
| STEP 7 | Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203 |
| Status | Nov 18, 2011 |
| Otatus | 1404 10, 2011 |